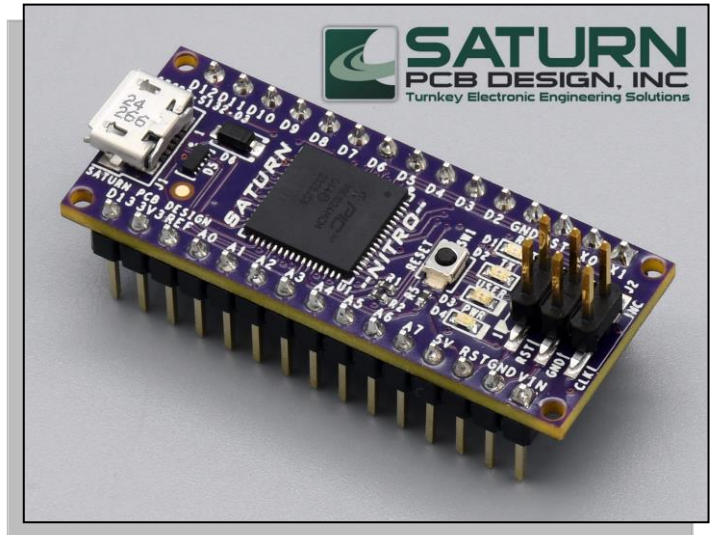


**—SATURN NITRO—**

Part Number: SPCBD-S132-03



**Description**

The **SATURN –NITRO–** is a low-cost solution for applications that need a professional, ultra powerful, high-speed plug-in processing unit. It utilizes a PIC32 IC for processing that runs at a far higher speed than other plug-in devices. Microchip MPLAB libraries available!

The PCB is manufactured using high temperature FR-4 for durability in extreme conditions.

For its CPU, the **SATURN –NITRO–** utilizes an extremely versatile and powerful PIC32MK1024MCM064 by Microchip Technology, Inc.

**Introduction Video:**

[Saturn PCB NITRO Introduction](#)



## Features

- Arduino Nano Compatible\*
- Vin: 6.5 to 20VDC
- 120MHz Operation
- 1MB Flash memory
- 256K x 8 RAM memory
- 4K x 8 EEPROM memory
- I2C, IrDA, LIN bus, PMP, SPI, UART/USART, USB OTG
- Independent simultaneous USB and UART communications.
- Operating Temperature: -0°C to 85°C
- Small PCB Size: 43.18mm x 17.78mm
- Customization available upon request.

\*This device only works with 3.3V logic levels.

## Power

- Micro USB 5V
- 6.5-20VDC input on pin B1 (VIN)
- 5V Regulated output on pin B4 (5V) Can also be used for 5V input.
- 3V3 Regulated output on pin B14 (3V3)

## I/O

- 20 Digital or 20 Analog depending on configuration.
- 7 12bit ADC modules @ 3.75msps
- 3 12bit DAC modules
- 12 PWM Output pairs
- 6 Quadrature encoder modules
- 8 DMA Channels
- 4 Op-Amps



# SATURN

## PCB DESIGN, INC

Turnkey Electronic Engineering Solutions

### Product Link:


<https://saturnpcb.com/nitro/>

### Applications

- Consumer electronics / military / aerospace

More information on the PIC32 can be found here:

<https://www.microchip.com/en-us/product/PIC32MK1024MCM064>



### PIC32MK GENERAL PURPOSE AND MOTOR CONTROL (GP/MC) FAMILY

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**32-bit General Purpose and Motor Control Application MCUs with FPU and up to 1 MB Live-Update Flash, 256 KB SRAM, 4 KB EEPROM, and Op amps**

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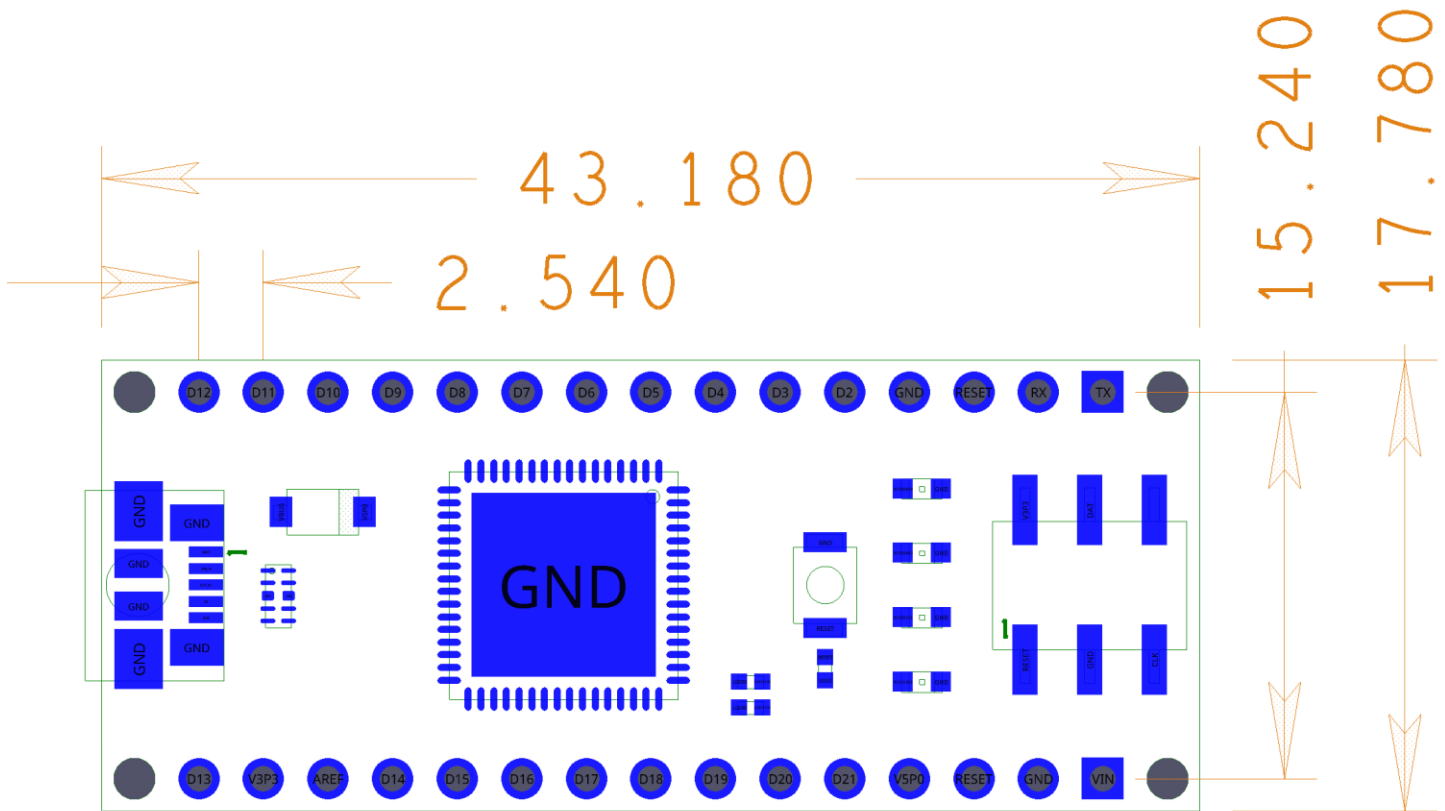
<p><b>Operating Conditions: 2.2V to 3.6V</b></p> <ul style="list-style-type: none"> <li>-40°C to +85°C, DC to 120 MHz</li> <li>-40°C to +125°C, DC to 80 MHz</li> </ul> <p><b>Core: 120 MHz (up to 198 DMIPS)</b></p> <ul style="list-style-type: none"> <li>MIPS32™ microAptiv™ MCU core with Floating Point Unit</li> <li>microMIPS™ mode for up to 40% smaller code size</li> <li>Opti-enhanced core:             <ul style="list-style-type: none"> <li>Four 64-bit accumulators</li> <li>Single-cycle MAC, saturating and fractional math</li> <li>Code-efficient (C and Assembly) architecture</li> </ul> </li> <li>Two 32-bit core register files to reduce interrupt latency</li> </ul> <p><b>Clock Management</b></p> <ul style="list-style-type: none"> <li>8 MHz ±5% (FRC) internal oscillator (OC) to +70°C</li> <li>Programmable PLL and oscillator clock sources:             <ul style="list-style-type: none"> <li>HS and EC clock modes</li> <li>Secondary USB PLL</li> <li>32 MHz Internal Low-power RC oscillator (LPRC)</li> <li>Independent external low-power 32 kHz crystal oscillator</li> <li>Fail-Safe Clock Monitor (FSCM)</li> <li>Independent Watchdog Timer (WDT) and Deadman Timer (DMT)</li> <li>Fast wake-up and start-up</li> <li>Four fractional clock out (REFCLKO) modules</li> </ul> </li> </ul> <p><b>Power Management</b></p> <ul style="list-style-type: none"> <li>Low-power management modes (Deep Sleep, Sleep, and Idle)</li> <li>Integrated:             <ul style="list-style-type: none"> <li>Power-on Reset (POR) and Brown-out Reset (BOR)</li> <li>On-board capacitorless regulator</li> </ul> </li> </ul> <p><b>Motor Control PWM</b></p> <ul style="list-style-type: none"> <li>Eight PWM pins</li> <li>Six additional Single-Shot PWM modules</li> <li>Dead-Time for rising and falling edges</li> <li>Dead-Time Compensation</li> <li>0.33 ns PWM Resolution</li> <li>Clock Chopping for High-Frequency Operation</li> <li>PWM Support for:             <ul style="list-style-type: none"> <li>DDC, ADC, Inverters, PFC, lighting</li> <li>BLDC, PMSM, ACMA, SRM motors</li> </ul> </li> <li>Choice of six Fault and Current Limit inputs</li> <li>Flexible Trigger Configuration for ADC Triggering</li> </ul> <p><b>Motor Encoder Interface</b></p> <ul style="list-style-type: none"> <li>Six Quadrature Encoder Interface (QEI) modules:             <ul style="list-style-type: none"> <li>Four inputs: Phase A, Phase B, Home, and Index</li> </ul> </li> </ul> <p><b>Audio/Graphics/Touch Interfaces</b></p> <ul style="list-style-type: none"> <li>External Graphics Interfaces through PMP</li> <li>Up to six I<sup>2</sup>S audio data communication interfaces</li> <li>Up to six SPI audio control interfaces</li> <li>Programmable audio master clock:             <ul style="list-style-type: none"> <li>Generation of fractional clock frequencies</li> <li>Can be synchronized with USB clock</li> <li>Can be tuned in runtime</li> </ul> </li> </ul> <p><b>Unique Features</b></p> <ul style="list-style-type: none"> <li>Permanent non-volatile 4-word unique device serial number</li> </ul> <p><b>Direct Memory Access (DMA)</b></p> <ul style="list-style-type: none"> <li>Up to eight channels with automatic data size detection</li> <li>Programmable Cyclic Redundancy Check (CRC)</li> <li>Up to 64 KB transfers</li> </ul>	<p><b>Security Features</b></p> <ul style="list-style-type: none"> <li>Advanced Memory Protection:             <ul style="list-style-type: none"> <li>Peripherals and memory region access control</li> </ul> </li> </ul> <p><b>Advanced Analog Features</b></p> <ul style="list-style-type: none"> <li>12-bit ADC module:             <ul style="list-style-type: none"> <li>Sum of all individual ADCs combined, 25.45 MspS 12-bit mode or 33.75 MspS 8-bit mode</li> <li>7 individual ADC modules</li> <li>3.75 MspS per 8-bit with dedicated DMA</li> <li>Up to 42 analog inputs</li> </ul> </li> <li>Flexible and independent ADC trigger sources</li> <li>Four Op amps and five Comparators</li> <li>Up to three 12-bit CDACs</li> <li>Internal temperature sensor ±2°C accuracy</li> <li>Capacitive Touch Divider (CVD)</li> </ul> <p><b>Communication Interfaces</b></p> <ul style="list-style-type: none"> <li>Up to four CAN modules with dedicated DMA channels:             <ul style="list-style-type: none"> <li>2.5B Active with DeviceNet™ addressing support</li> </ul> </li> <li>Up to six UART modules (up to 25 Mbps):             <ul style="list-style-type: none"> <li>Supports LIN 1.2 and IEC™ protocols</li> </ul> </li> <li>Six SPI/F<sup>2</sup>S modules (SPI 50 Mbps)</li> <li>Parallel Master Port (PMP)</li> <li>Up to two F<sup>2</sup>S USB 2.0-compliant On-The-Go (OTG) controllers</li> <li>Peripherals Pin Select (PPS) to enable nonunique pin functions</li> </ul> <p><b>Timers/Output Compare/Input Capture/RTCC</b></p> <ul style="list-style-type: none"> <li>Up to 14 16-bit or one 16-bit and eight 32-bit timers/counters for GP and MC devices and six additional QEI 32-bit timers for MC devices</li> <li>16 Output Compare (OC) modules</li> <li>16 Input Capture (IC) modules</li> <li>PPS to enable function remap</li> <li>Real-Time Clock and Calendar (RTCC) module</li> </ul> <p><b>Input/Output</b></p> <ul style="list-style-type: none"> <li>5V-tolerant pins with up to 22 mA source/sink</li> <li>Selectable internal open drain, pull-ups, and pull-downs</li> <li>External interrupts on all I/O pins</li> <li>Five programmable edge-triggered interrupt pins</li> </ul> <p><b>Qualification and Class B Support</b></p> <ul style="list-style-type: none"> <li>AEQ-Q100 REV3 (grade 1 -40°C to +125°C) (planned)</li> <li>Class B Safety Library, IEC 60730 (planned)</li> <li>Back-up internal oscillator</li> <li>Clock monitor with back-up internal oscillator</li> <li>Global register locking</li> </ul> <p><b>Debugger Development Support</b></p> <ul style="list-style-type: none"> <li>In-circuit and in-system programming</li> <li>2-wire or 4-wire MIPS® Enhanced JTAG interface</li> <li>Unlimited software and 12 complex breakpoints</li> <li>IEEE 1149.2-compatible (JTAG) boundary scan</li> <li>Non-invasive hardware-assisted instruction trace</li> </ul> <p><b>Software and Tools Support</b></p> <ul style="list-style-type: none"> <li>C/C++ compiler with native Discretionary support</li> <li>MPLAB® Harmony Integrated Software Framework</li> <li>TCPIP, USB, Graphics, and I/O™™ middleware</li> <li>MPF, Android™ and Bluetooth™ audio frameworks</li> <li>RTDS Kernel: Express Logic ThreadX, FreeRTOS™, ORIENTOS™, Micrium™ uCOS™, and SEGGER™ emCoS®</li> </ul>
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**Dimensions**

L•W•H: 143.18mm x 17.78mm x 20mm





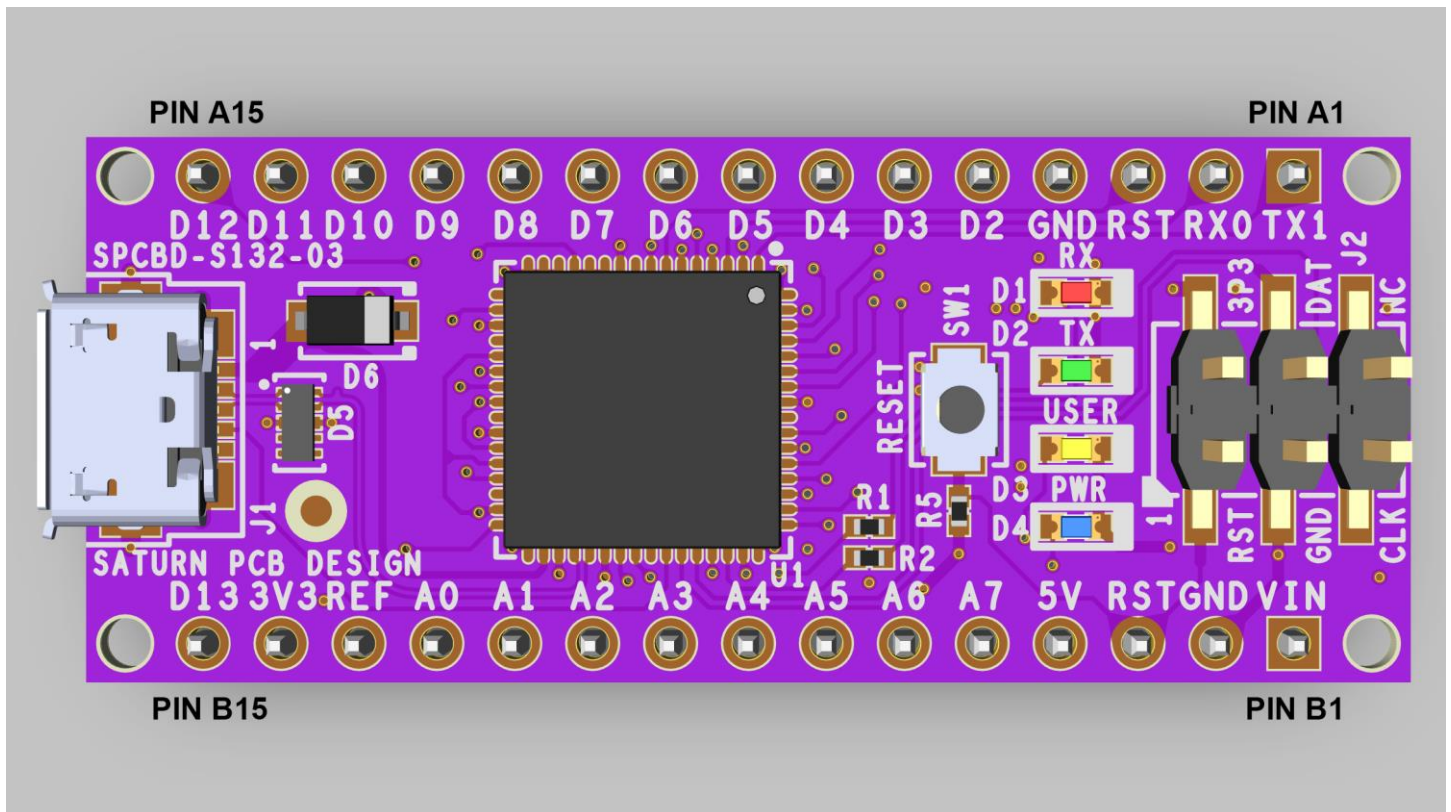
## J2 Pinnout

Amphenol ICC Connector: 54202-S0803LF

- 1: Reset
- 2: V3P3
- 3: GND
- 4: DAT
- 5: CLK
- 6: NC

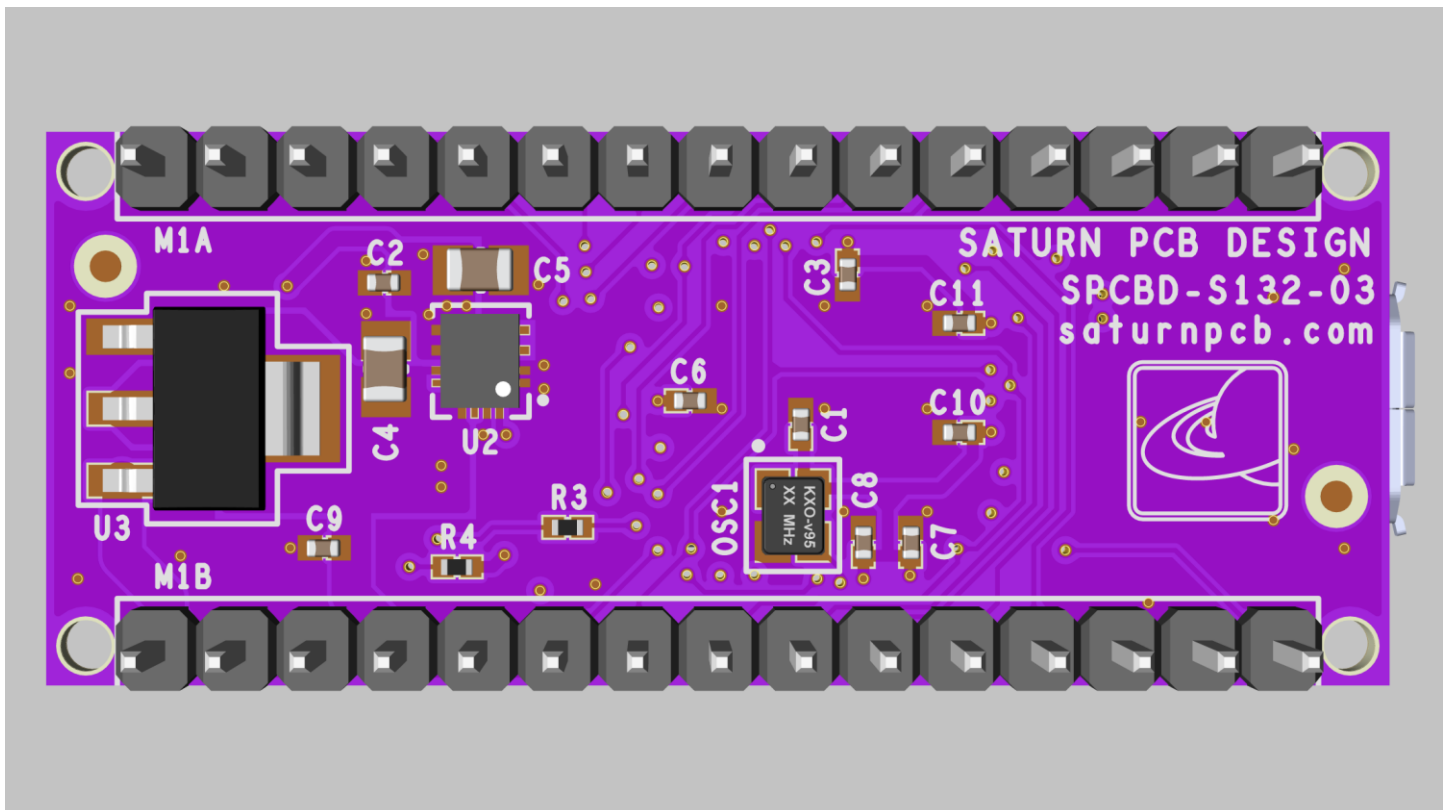


Top View



- D1: Data Receive
- D2: Data Transmit
- D3: User controlled
- D4: V3P3 On

**Bottom View**





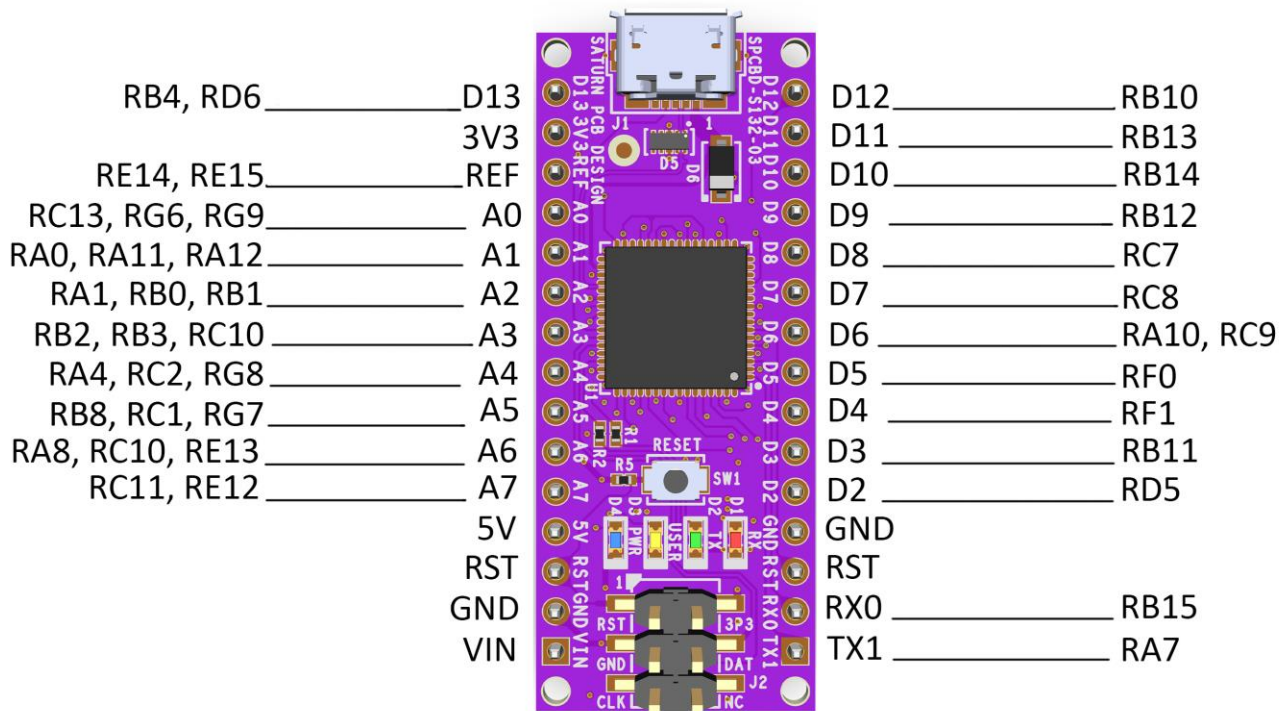
## Header Pinnouts

		<u>M1A</u>		<u>M1B</u>		
		PH1-15-UA		PH1-15-UA		
TX	1				1	VIN
RX	2	A1		B1	2	GND
RESET	3	A2		B2	3	RESET
GND	4	A3		B3	4	V5P0
D2	5	A4		B4	5	D21
D3	6	A5		B5	6	D20
D4	7	A6		B6	7	D19
D5	8	A7		B7	8	D18
D6	9	A8		B8	9	D17
D7	10	A9		B9	10	D16
D8	11	A10		B10	11	D15
D9	12	A11		B11	12	D14
D10	13	A12		B12	13	AREF
D11	14	A13		B13	14	V3P3
D12	15	A14		B14	15	D13
		A15		B15		



## SATURN –NITRO–

### Pin Assignment Work Sheet



TX, RX, D2 to D13 are all 5V tollerent